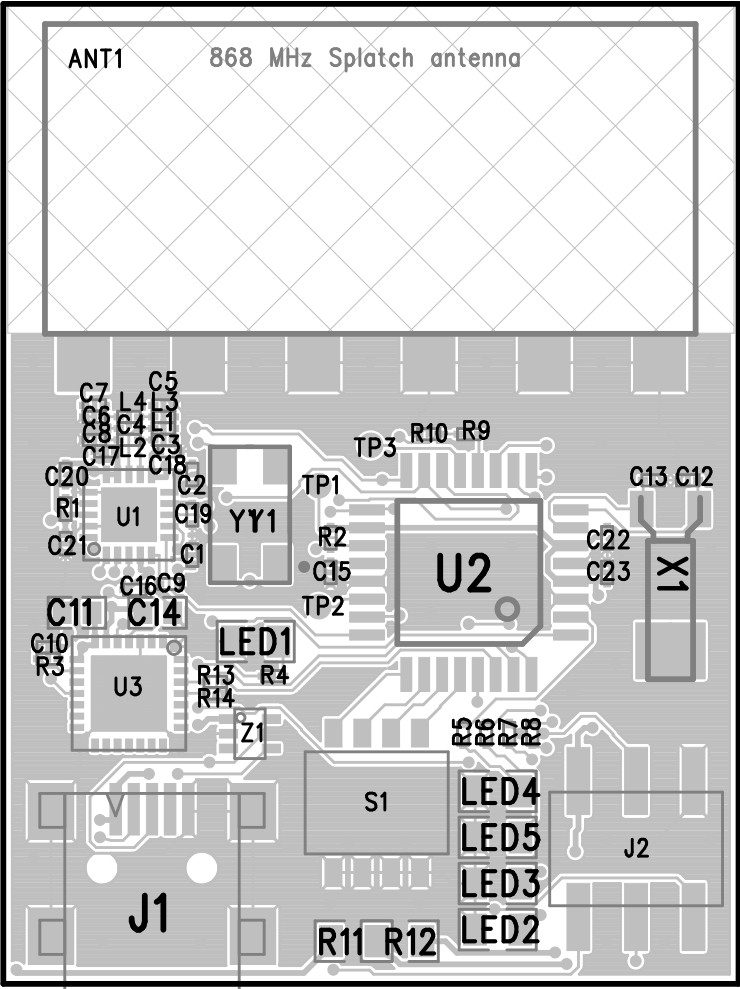
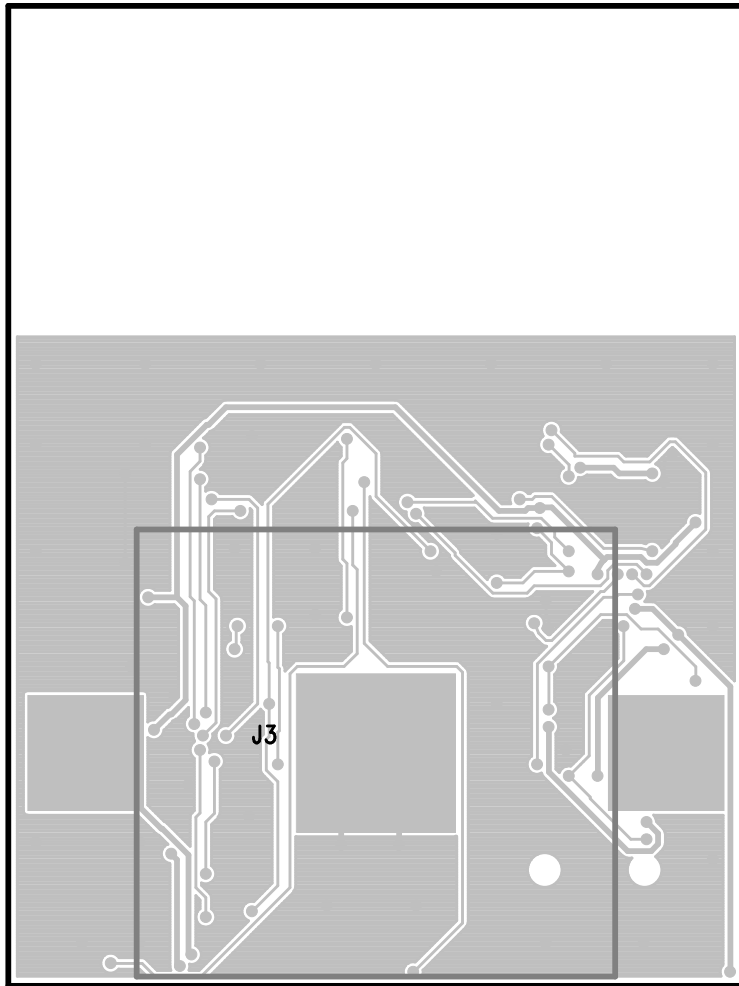
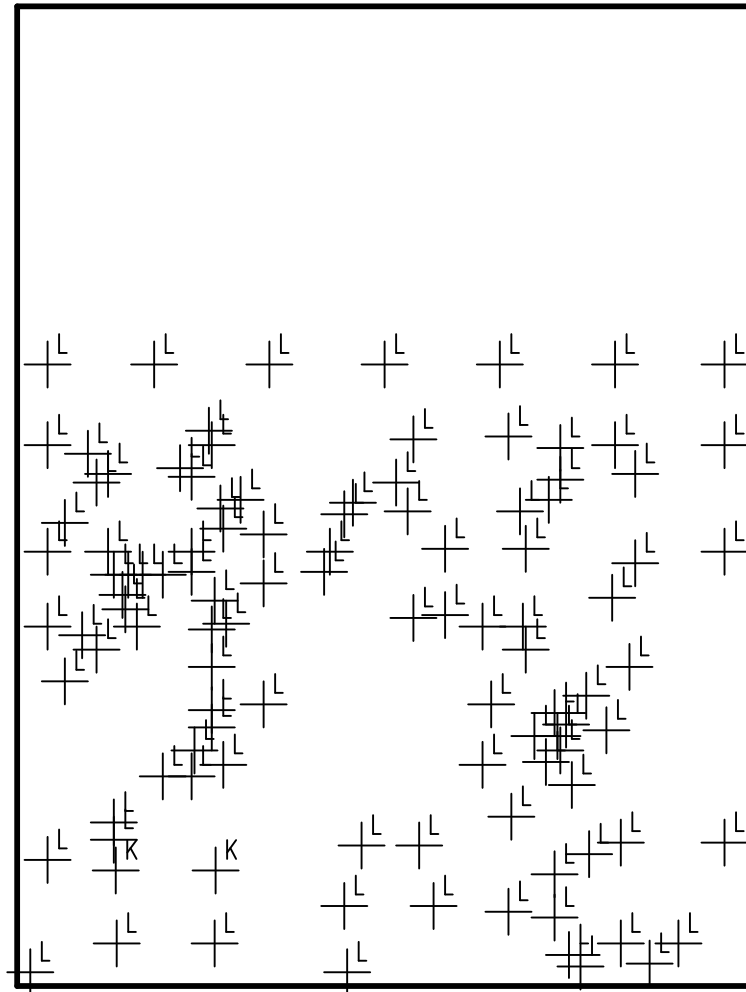




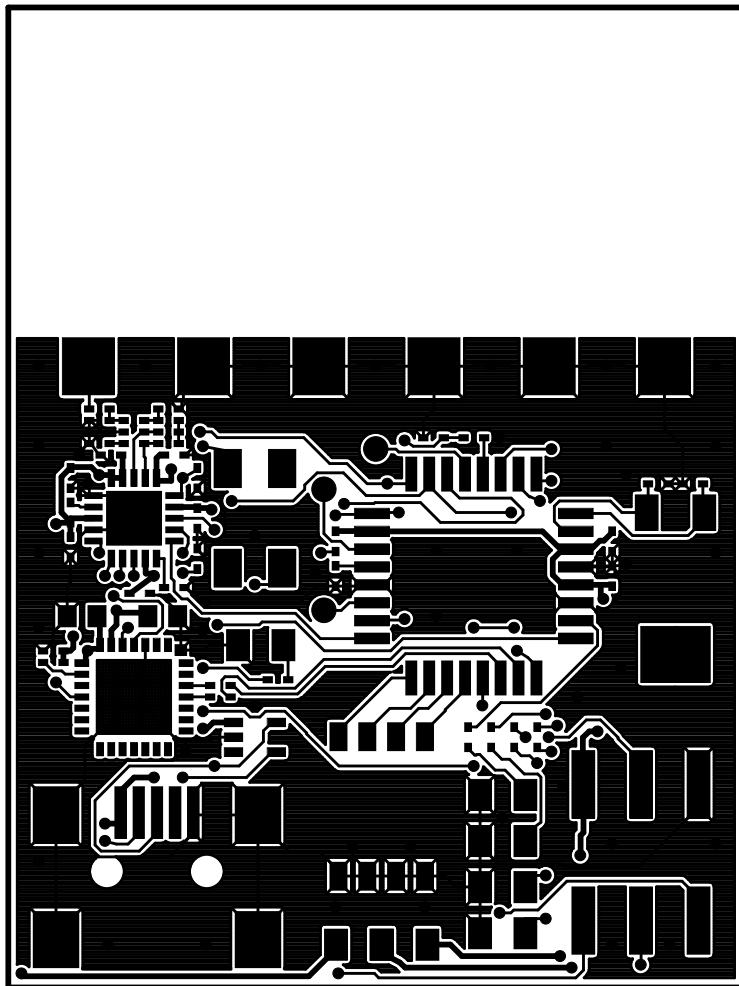
Top Assembly Drawing Top



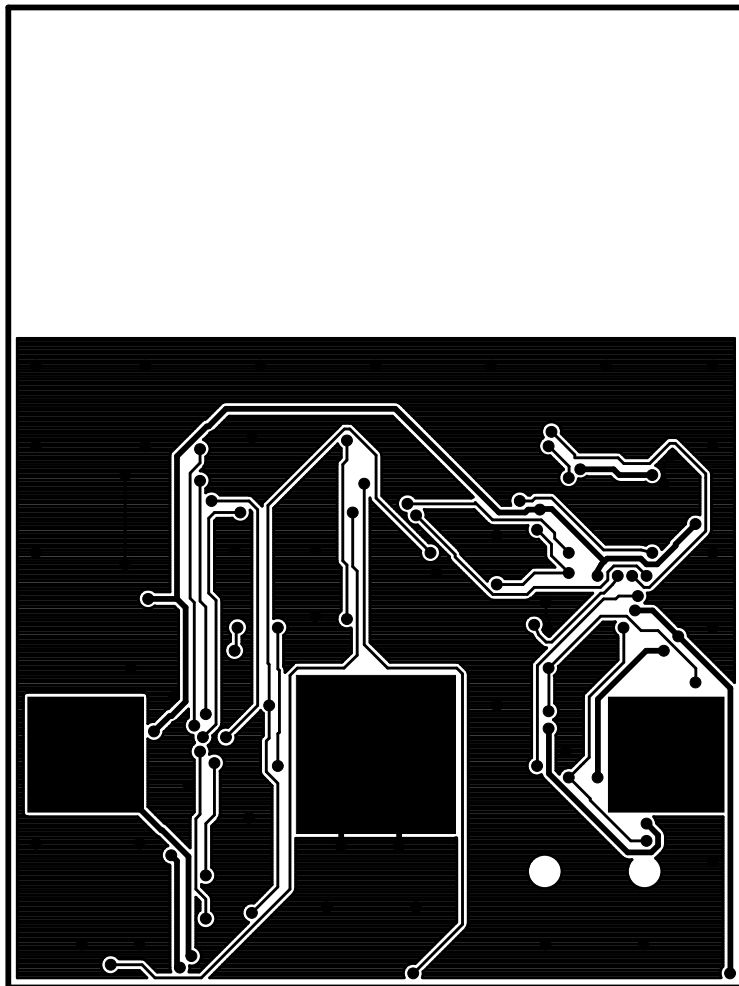
Assembly Drawing Bottom



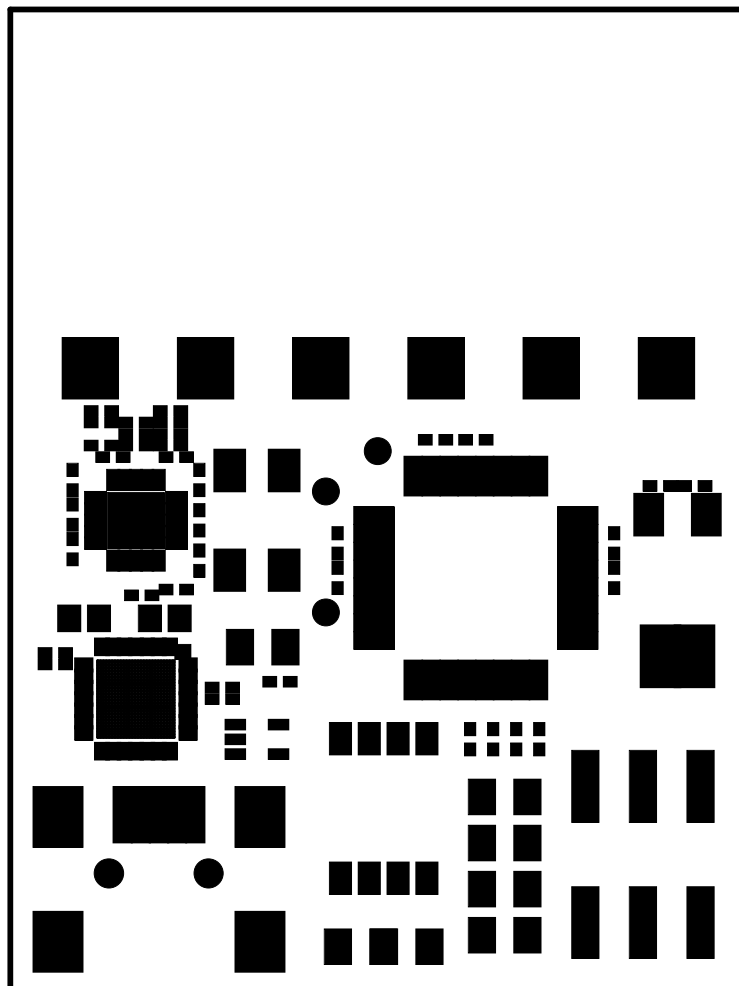
SIZE	QTY	SYM	PLATED	TOL
0.9	2	$\begin{array}{c} \text{---} \\ \\ \text{---} \end{array}^{\text{K}}$	NO	+/-0.0
0.3	107	$\begin{array}{c} \text{---} \\ \\ \text{---} \end{array}^{\text{L}}$	YES	+/-0.0



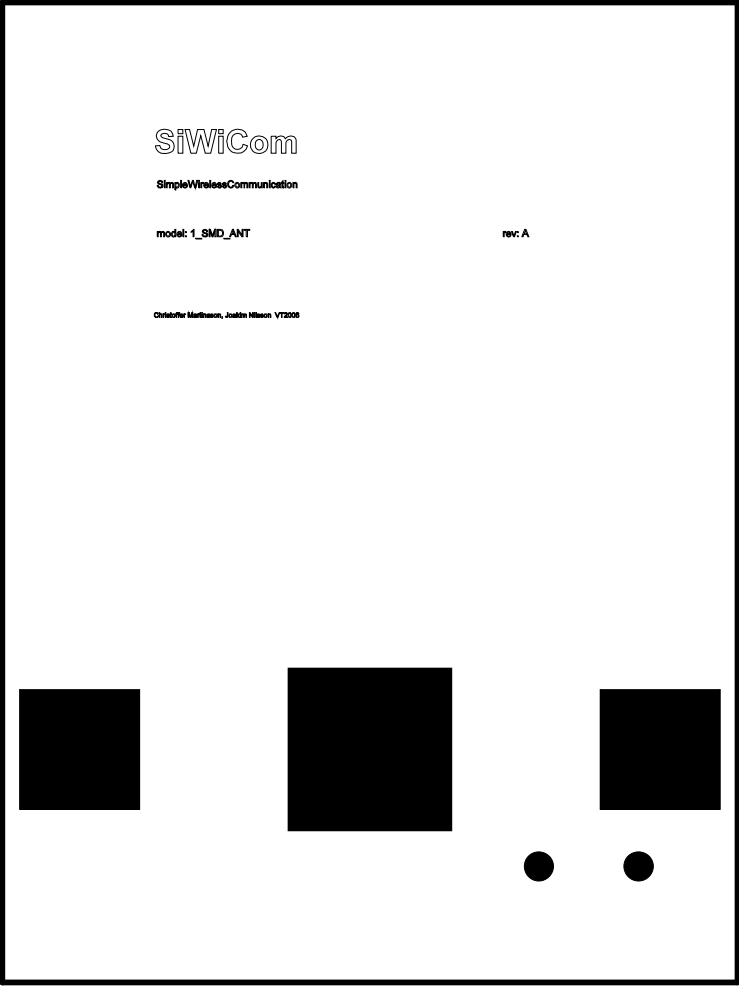
Top



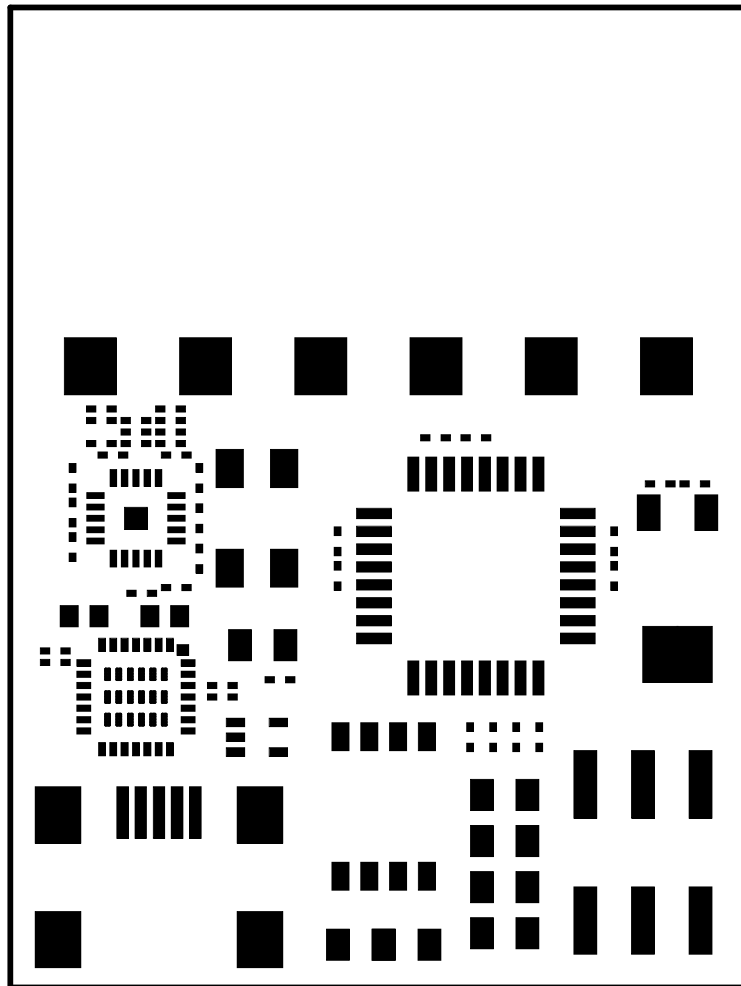
Bottom



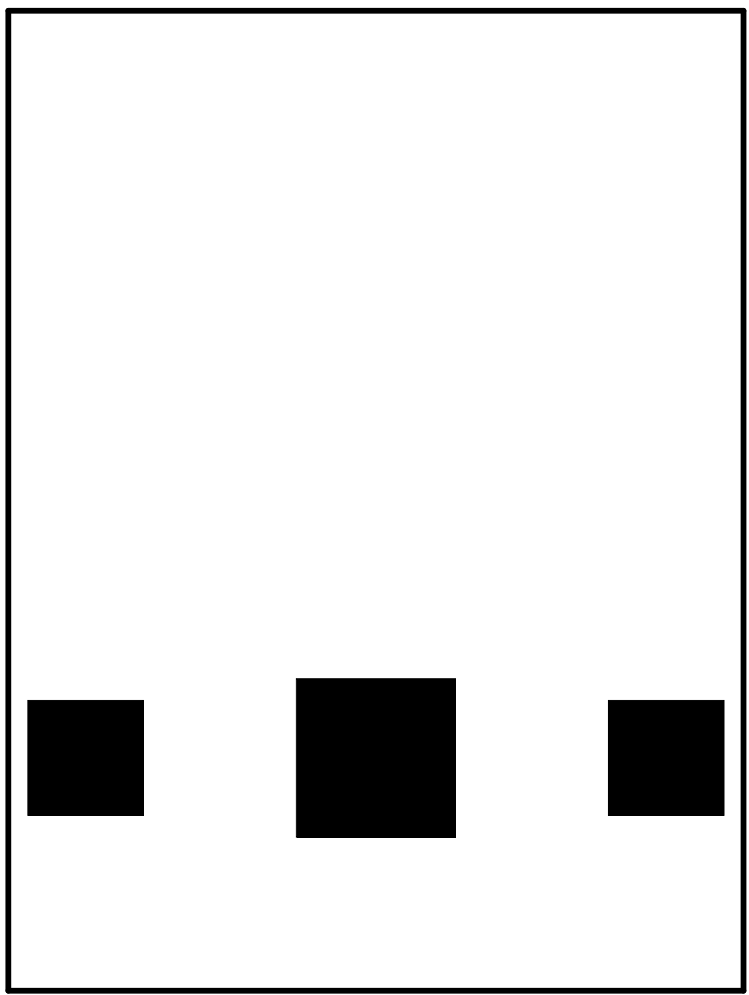
Solder Mask Top



Solder Mask Bottom



Paste Mask Top



Past Mask Bottom